



#25/1A8  
3-28-03  
R. Grader

Inventor: **THOMAS T. Moore**

Title: **Method of Forming Oxide Regions Over Semiconductor Substrates**

Assignee: **Micron Technology, Inc.**

**INFORMATION DISCLOSURE STATEMENT**

References – See Attached Form PTO-1449

The attached form PTO-1449 is submitted in compliance with 37 CFR §1.56. No admission is made regarding whether any of the submitted references is prior art. Copies of the references are attached.

Respectfully submitted,

Dated: February 24, 2003

Attorney:

Jennifer J. Taylor  
Jennifer J. Taylor, Ph.D.  
Reg. No. 48,711

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